

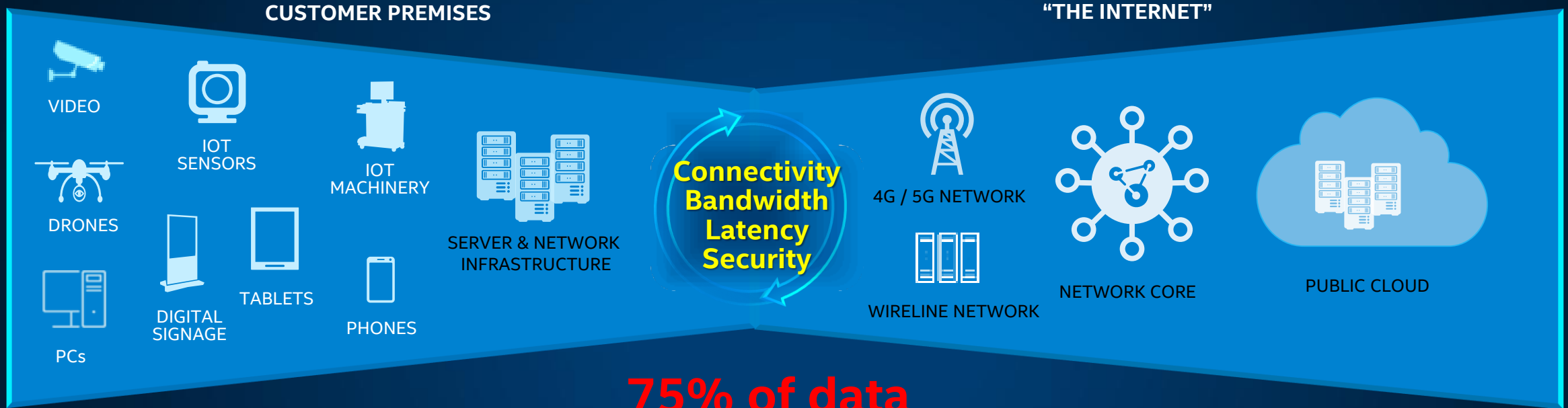
Intel边缘云解决方案与最佳实践

许渊 Intel资深解决方案架构师



intel[®]

EXPANDING IOT/IT/CLOUD TRANSFORMATION INCREASED EDGE PAIN POINTS



75% of data
will be created outside of data centers by 2025*

Connectivity

Insufficient coverage
Per-device cost structure

Bandwidth

Data growth → higher costs

Latency

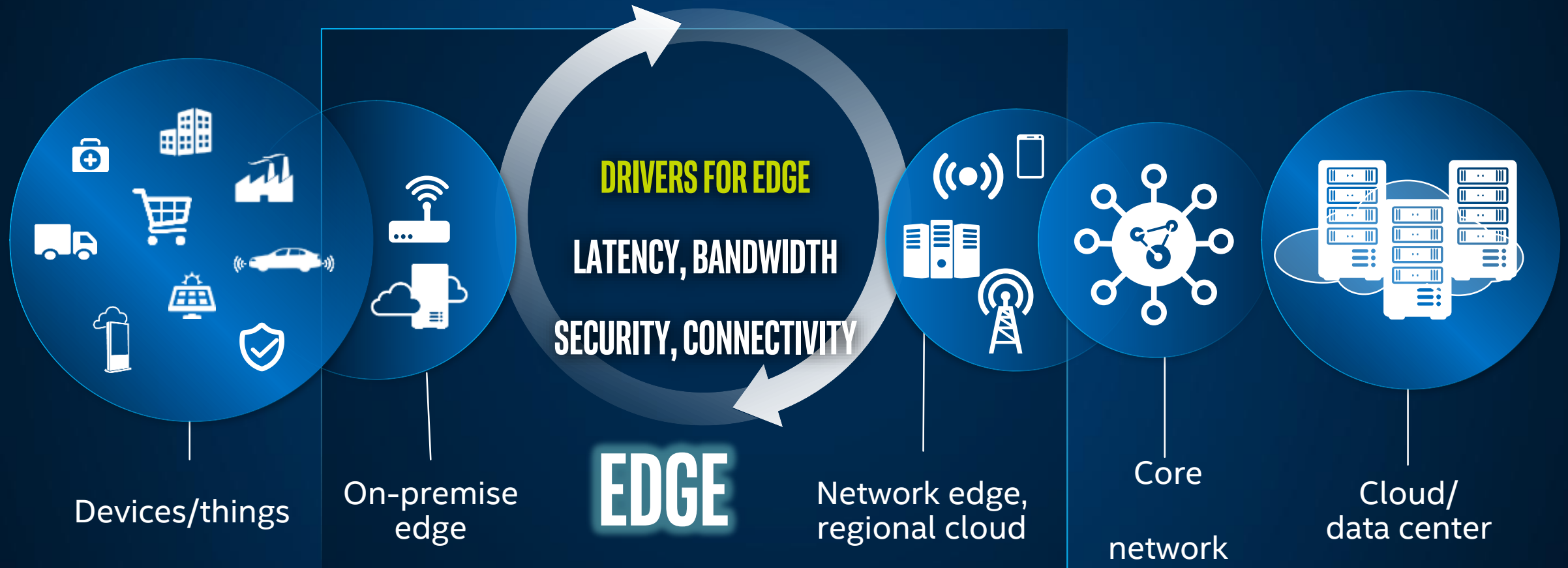
Long routes limit
real-time processing

Security

Data traverses' multiple
networks and resides
"in the cloud"

*Gartner: <https://www.gartner.com/smarterwithgartner/what-edge-computing-means-for-infrastructure-and-operations-leaders>

THE EDGE: PLACEMENT OF COMPUTE CLOSER TO THE DATA SOURCE OR POINT OF SERVICE DELIVERY



AI and 5G are major accelerants for edge computing

EDGE COMPUTING SCENARIO

- V** Volume and Cost of Data Transmission
- L** Latency to/from Cloud
- C** Lack of Persistent Connectivity
- P** Privacy: Secure the Data Locally, Isolation from public Data Center
- W** Workload Orchestration
- A** Analytics and AI

EDGE CLOUD

MARKET SEGMENTS AND USE CASES



VISION INSPECTION

INDUSTRIAL

HEALTHCARE

SMART CITY

RETAIL

TELCO

- L** **V** **A** Defect Detection
Anomaly Detection
- L** **A** **P** Instruments Reading
Object Identification

- L** **A** Predictive Maintenance
- W** **L** **P** Factory Automation

- V** **P** Clinical Fusion

- W** **C** Traffic Monitoring

- W** **A** Personalized

- L** **W** **A** MEC

INTEL DIVERSE PRODUCT PORTFOLIO

BUILDING FOR THE EDGE



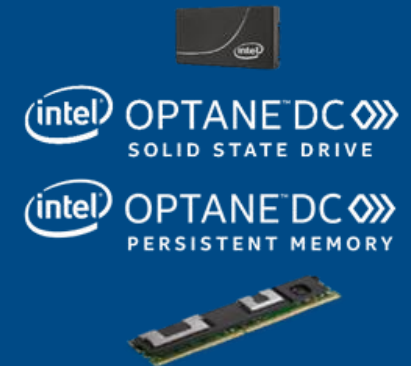
PROCESS



MOVE



STORE



*Other names and brands names may be claimed as the property of others.

Delivering AI compute from cloud to edge



OpenVINO

Caffe

GLFW

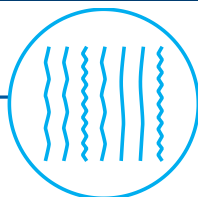
mxnet

ONNX
RUNTIME

PaddlePaddle

PYTORCH

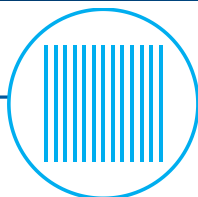
TensorFlow



CPU | SCALAR



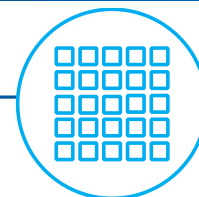
Intel Xeon
Scalable processors



GPU | VECTOR



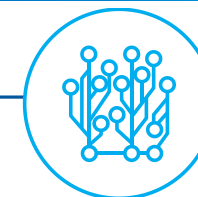
Intel
Discrete Graphics



FPGA | SPATIAL



Intel
FPGA



SPECIALIZED | MATRIX

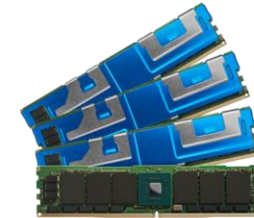


Habana
Intel Movidius Myriad
Intel Mobileye EyeQ

Transforming memory & storage



Up to
4.5TB
TOTAL MEMORY
per socket

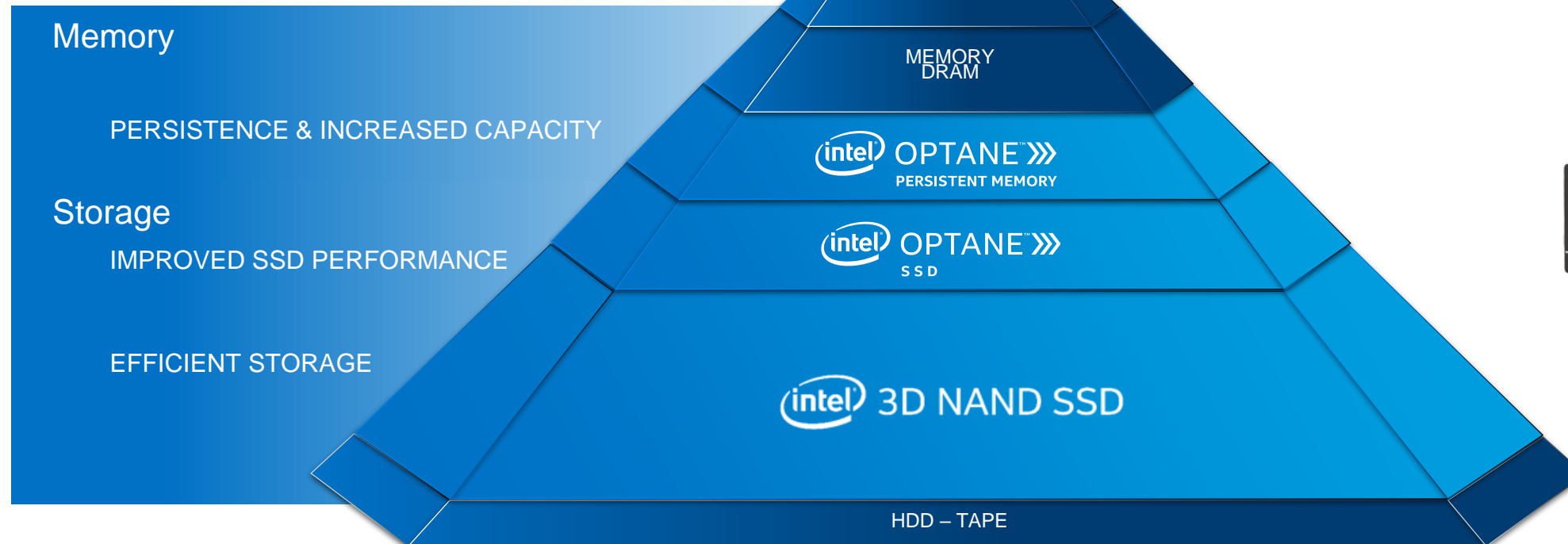


Average
25%
HIGHER MEMORY
BANDWIDTH⁷
vs. prior gen

**REDUCE I/O
BOTTLENECKS**
to analyze data faster

Over
225X
FASTER ACCESS
TO DATA⁸
vs. mainstream NAND SSD

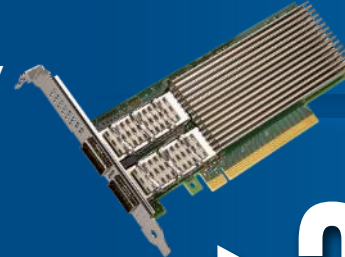
**BOOST
APPLICATION
PERFORMANCE**



INTEL® ETHERNET 800 SERIES WITH APPLICATION DEVICE QUEUES (ADQ)

Application Device Queues

- An application-specific queuing and steering technology
- Filters application traffic to a dedicated set of queues



>45% LATENCY REDUCTION¹¹

WITH OPEN SOURCE REDIS USING 2ND GEN INTEL® XEON® SCALABLE PROCESSORS AND INTEL® ETHERNET 800 SERIES WITH ADQ

>30% THROUGHPUT IMPROVEMENT¹²

WITH OPEN SOURCE REDIS USING 2ND GEN INTEL® XEON® SCALABLE PROCESSORS AND INTEL® ETHERNET 800 SERIES WITH ADQ



BETTER TOGETHER
CONSISTENTLY IMPROVED SERVICES AND SLA
WITH 2ND GEN INTEL® XEON® SCALABLE PROCESSORS
AND INTEL® ETHERNET 800 SERIES

INCREASES
APPLICATION
PREDICTABILITY



REDUCES
APPLICATION
LATENCY



IMPROVES
APPLICATION
THROUGHPUT



CONSISTENT
APPLICATION RESPONSE
TIME WITH ADQ

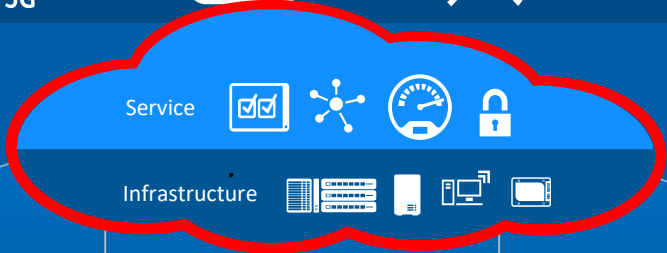
For more complete information about performance and benchmark results, visit www.intel.com/benchmarks. See configuration slide 50 for details. For more information regarding performance and optimization choices in Intel software products, please visit <https://software.intel.com/en-us/articles/optimization-notice>.

EDGE CLOUD SOLUTION ARCHITECT

DEVICE
Generate Data



EDGE
First tier Data
Governance



EDGE SAAS

**UNIFIED
INFRASTRUCTURE**



**UNIFIED
ORCHESTRATION**



**UNIFIED
DATA BUS**



EDGE PAAS

CLOUD
Full Data
Process/Store
Control Center

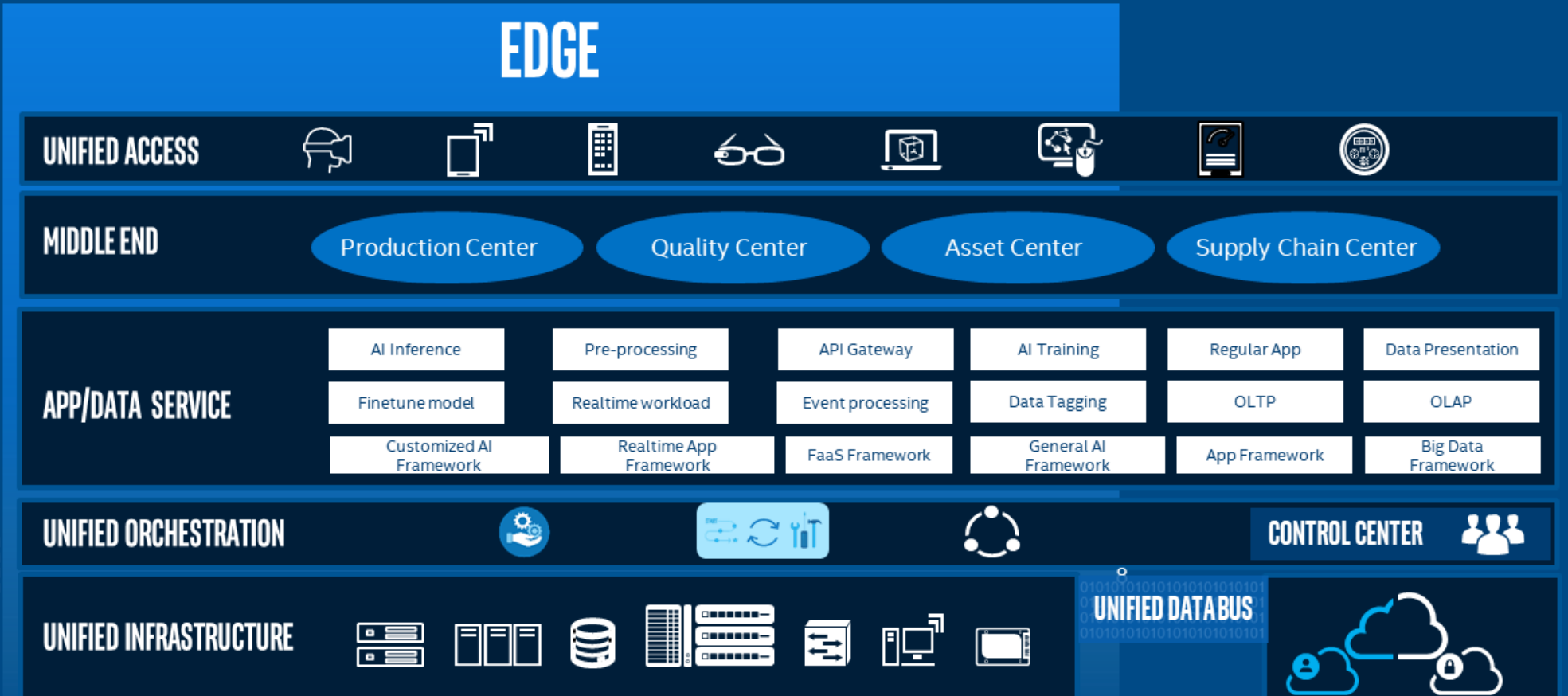


EDGE IAAS

EDGE SIDE ARCHITECT



010101010101
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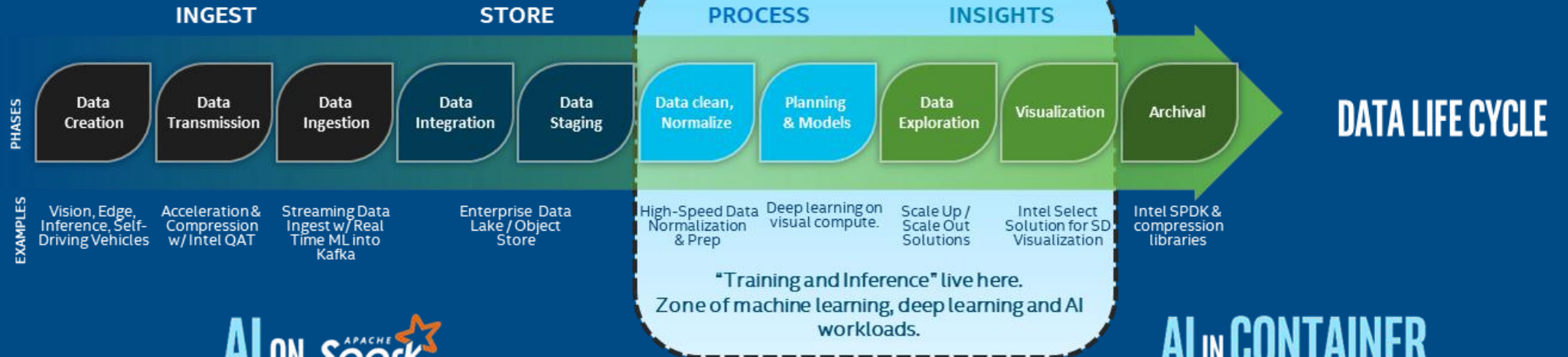


EDGE CLOUD AI PLATFORM

INFERENCE

INTEL® DISTRIBUTION OF OPENVINO™ TOOLKIT

Take your computer vision solutions to a new level with deep learning inference intelligence



TRAINING

AI ON SPARK

BigDL

HIGH-PERFORMANCE DEEP LEARNING FRAMEWORK FOR APACHE SPARK

<https://github.com/intel-analytics/bigdl>

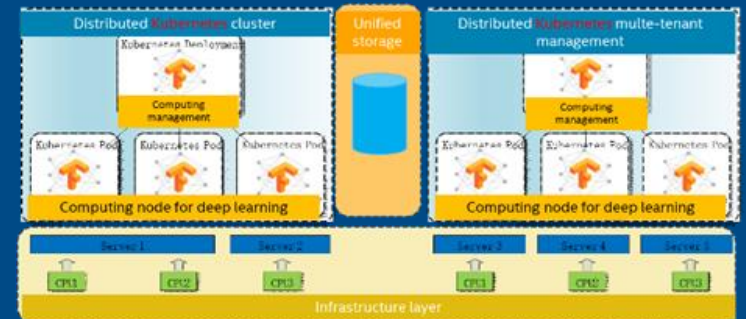
ANALYTICS ZOO

ANALYTICS + AI PIPELINES FOR DISTRIBUTED TENSORFLOW, KERAS AND BIGDL ON APACHE SPARK

Reference Use Cases, AI Models, High-level APIs, Feature Engineering, etc.
<https://github.com/intel-analytics/analytcs-zoo>

UNIFYING ANALYTICS + AI ON APACHE SPARK

AI IN CONTAINER



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EDGE SOFTWARE OFFERINGS

ECOSYSTEM SOLUTIONS

intel market ready ✓

intel RFP Ready ✓

intel select solution



INDUSTRIAL



ROBOTICS



RETAIL



HEALTHCARE



CITIES



TRANSPORTATION

USE CASE-SPECIFIC SOFTWARE

EDGE INSIGHTS FOR INDUSTRIAL

EDGE INSIGHTS FOR RETAIL

CONVERGED EDGE

MORE IN 2020



Edge & Cloud Orchestration



Use Case-Specific Implementations



Multi-Cloud Capability



5G Network Readiness



Multi-Workload

DEVELOPER TOOLKITS

OpenVINO

DEEP LEARNING ACCELERATION

OpenNESS

5G NETWORK TRANSFORMATION

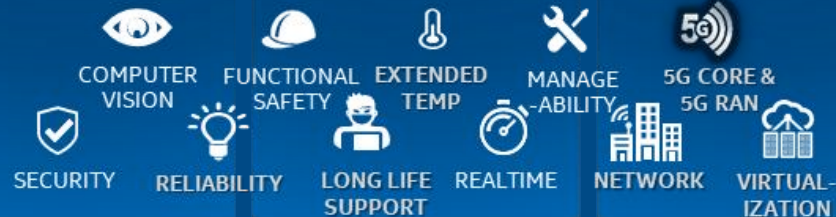


MEDIA EXPERIENCE EVOLUTION

MORE IN 2020...

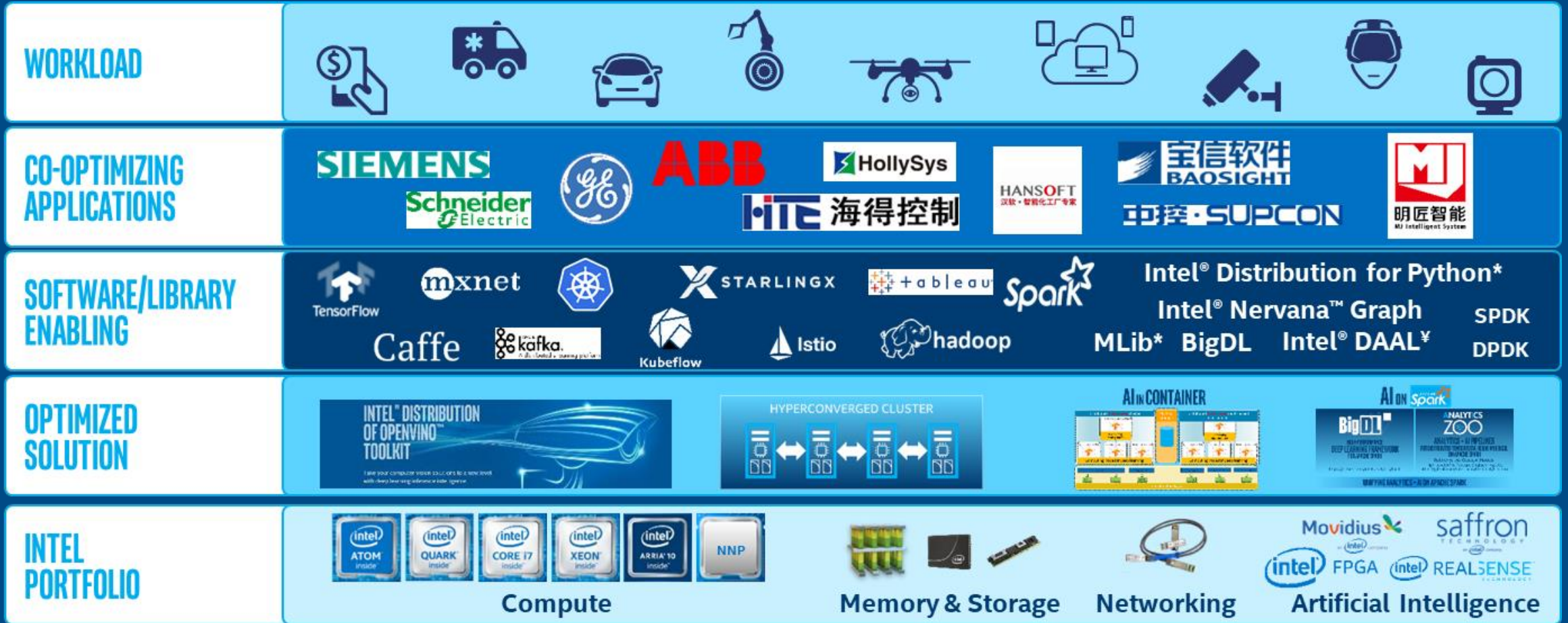


Hardware Scalability & Built-In Edge Features



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EDGE CLOUD SOLUTION LANDSCAPE



¥Note: Intel® Data Analytics Acceleration Library, Intel® Math Kernel Library, Intel® Math Kernel Library for Deep Neural Networks, BigDL: Distributed Deep Learning on Apache Spark*, MLib: Apache Spark's Scalable Machine Learning Library

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